# **WISPNET 2023**



International Conference on Wireless communications, Signal Processing and NETworking (TECHNICALLY CO-SPONSORED BY THE IEEE)

MARCH 29-31, 2023

### **ABOUT THE CONFERENCE**

The 3-day conference will feature online and inperson keynote and invited talks, and in-person presentation of accepted contributed papers. The organizers aspire to make the event a professionally and personally rewarding one. To this end, most of the invited speakers will stay through the duration of the conference in the venue to interact with participants. It is also desired that the participants attend most of the sessions and interact with other participants and speakers so as to reap the full benefits of a conference. Attendance in 75% of the sessions will be a requirement for participants needing a certificate of attendance.

## PAPER SUBMISSION GUIDELINES

Papers should be 4 - 5 pages in length, and in the IEEE conference paper template. The templates are available at the following link:

https://www.ieee.org/conferences/publishing/templates.html

Please use the following link to submit papers:

https://easychair.org/conferences/?conf=wispnet2023

All submitted papers will be subject to an initial screening, including similarity checks, and those qualifying will undergo a peer review process.
All accepted and presented papers will be submitted to IEEE Xplore for possible publication.

#### **ORGANISED BY**

Department of Electronics and Communication Engineering, Sri Sivasubramaniya Nadar (SSN) College of Engineering, Kalavakkam 603110, India

#### **VENUE**

Second Floor, Career Development Centre, SSN College of Engineering

## **CALL FOR PAPERS**

Papers are invited in all areas of electronics and communication engineering, including, but not limited to, the following:

- Wireless Communication and Networking
- Cryptography in Wireless Networks
- AI & ML for Engineering Applications
- IoT in Communication Engineering
- Electromagnetics and Antenna Engineering
- Speech and Image Processing
- Acoustic Underwater Communication
- Deep Learning, Neural Languages and NLP
- Embedded and Autonomous Systems
- MEMS and Nano Electronics
- VLSI Design and Testing
- Bioelectronics and Bioinformatics
- Energy Harvesting Techniques

Full paper submission deadline
Notification of acceptance
Final paper submission deadline
Registration start date
31.01.2023
28.02.2023
10.03.2023

Please note that the deadlines are hard.

#### **SPEAKERS** (As on 29.12.2022)

MR. AJAY KUMAR
VAIDHIYANATHAN
Intel, Bengaluru
Advanced 2D materials in
current electronic system
design

DR. T. NANDHA KUMAR

University of Nottingham Malaysia Campus An overview of SRAM and ReRAM-based In-memory computing DR. HUGO ESPINOSA

Griffith University,
Australia
Wearable sensors:
Challenges in sports, health
and forensics

DR. UDAY KHANKHOJE

IIT Madras Fault diagnosis in phased antenna arrays