

Name of the Department*	<b>Electronics and Communication Engineering</b>		
Course	<b>M. E. (VLSI Design)</b>		
Level	<b>PG</b>		
1 <sup>st</sup> Year of approval by the Council	2011 (with an intake of 18)		
	CAY 2015-16	CAY-1 2014-15	CAY-2 2013-14
Year wise Sanctioned Intake	36	36	36
Year wise Actual Admissions	34	32	36
Cut off marks – General Quota	-	41.056	45.019
% Students passed with Distinction	6	11	6
% Students passed with First Class	28	25	25
Students Placed	16	17	8
Average Pay packages, Rs. In Lakh /Year	3.65	3.25	3.80
Students Opted for Higher Studies	2	3	4
Accreditation Status of the course	Not Accredited		
Doctoral Courses	Ph.D (Information and Communication Engg.) Anna University Chennai		
Foreign Collaborations, if any	-		
Professional Society Memberships	IEEE, IEEE Communication Society, IETE, ISTE		
Professional Activities	31		
Consultancy Activities	3		
Grants Fetched	External Funded projects: 12 Nos worth 250 Lakh Internal Funded projects: 15 Nos worth 69 Lakh		
Departmental Achievements	University Ranks: 14 (M.E. VLSI Design) Publications: International Journals – 76 Conference - 113		
Distinguished Alumni	R. Bharathi, S. Chrisben Gladson		